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PATENT

Application No.: 10/676,889
Filing Date: Sept. 30, 2003
First Named Inventor: Shih-wei Liao
Examiner's Name: Not yet assigned
Art Unit: Not yet assigned
Confirmation No.: Not yet assigned
Attorney Docket No.: 42P16806



☐ An Amendment After Final Action (37 CFR 1.116) is attached and applicant(s) request expedited action.
☒ Charge any fee not covered by any check submitted to Deposit Account No. 02-2666.
☒ Applicant(s) hereby request and authorize the U.S. Patent and Trademark Office to (1) treat any concurrent or future reply that requires a petition for extension of time as incorporating a petition for extension of time for the appropriate length of time and (2) charge all required fees, including extension of time fees and fees under 37 CFR 1.16 and 1.17, for any concurrent or future reply to Deposit Account No. 02-2666.
☐ Applicant(s) claim small entity status (37 CFR 1.27).

ATTACHMENTS

☒ Postcard (Return Receipt)
☐ Fee Transmittal Document (that includes a fee calculation based on the type and number of claims)
☐ Check(s)
☐ Petition for Extension of Time
☐ Response to Missing Parts
☐ Amendment/Response with respect to Office Action
☐ Amendment/Response After Final Action (37 CFR 1.116) (reminder: consider filing a Notice of Appeal)
☐ Notice of Appeal
☐ RCE (Request for Continued Examination)
☐ Supplemental Declaration
☐ Terminal Disclaimer (reminder: if executed by an attorney, the attorney must be properly of record)
☒ Information Disclosure Statement (IDS) and 1449
☒ Copies of IDS citations
☐ Cross-Reference to Related Application(s)
☐ Certified Copy of Priority Document
☐ Other: _____
☐ Other: _____

SUBMITTED BY:

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

TYPED OR PRINTED NAME: Kevin G. Shao

SIGNATURE: 

REG. NO.: 45,095

DATE: January 9, 2006

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria Virginia 22313-1450 on January 9, 2006

Date of Deposit

David Castro

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January 9, 2006

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(10/14/03)



Attorney's Docket No. 42P16806

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shih-wei Liao

Application No.: 10/676,889

Filed: Sept. 30, 2003

For: METHODS AND APPARATUSES FOR
COMPILER-CREATING HELPER THREADS
FOR MULTI-THREADING

Examiner: Not yet assigned

Art Unit: Not yet assigned

Conf. No.: Not yet assigned

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

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I, David Castro, hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop Amendment to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450 on January 9, 2006 by: David Castro

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:


- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
- (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

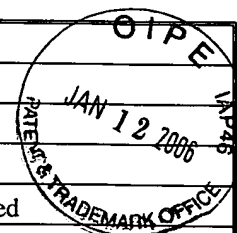
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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/676,889
				Filing Date	Sept. 30, 2003
				First Named Inventor:	Shih-wei Liao
				Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	1	of	1	Attorney Docket Number	42P16806
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		HING WANG ET AL: "Speculative precomputation: exploring the use of multithreading for latency" INTEL TECHNOLOGY JOURNAL, vol. 6, no.1, February 14, 2002, XP002303432, pp 1-14.			
		CSABA ANDRAS MORITZ: "Tutorials PACT 2003", PACT 2003 (12 th ANNUAL INTERNATIONAL CONFERENCE ON PARALLEL ARCHITECTURE AND COMPILEATION TECHNIQUES) CONFERENCE PROGRAM – TUTORIALS, September 16, 2003, XP002348822, URL: http://www.ecs.umass.edu/ece/andras/pact_2003_tutorials.htm			
		YEUNG D: "Tutorial on Architecture and Compiler Support for Speculative Precomputation" TUTORIAL OUTLINE (PACT 2003), XP002348826, URL: http://maggini.eng.umd.edu/vortex/tutorial			
		LIAO: "Speculative Precomputation on Intel Architectures" TUTORIAL ON ARCHITECTURE AND COMPILER SUPPORT FOR SPECLATIVE PRECOMPUTATION – 12 TH ANNUAL INTERNATIONAL COINFERENCE ON PARALLEL ARCHITECTURES AND COMPILEATION TECHNIQUES – NEW ORLEANS, LOUISIANA, SEPT. 27-OCT.1, 2003, XPoo2348823, URL: http://maggini.eng.umd.edu/vortex/tutorial/slides/industry.pdf			
		DORAI: "Optimizing SMT Processors for High Single-Thread Performance", The Journal of Instruction-Level Parllelism, vol. 5, April 2003, XP002348824, URL: http://www.jilp.org/vol15/v5paper3.pdf			
		LIAO: "Post-pass binary adaptation for software-based speculative precomputation", ACM SIGPLAN NOTICES, ACM, ASSOCIATION FOR COMPUTING MACHINERY, NEW YORK, NY, US vol. 37, no. 5, May 2002, pp. 117-128, XP002302652			
		XINMIN TIAN: "Intel ® Open MP C++/Fortran Compiler for Hyper-Threading Technology: Implementation and Performance" INTEL TECHNOLOGY JOURNAL INTEL CORP USA, vol. 6, no.1, February 14, 2002, pp. 36-46, XP002348825, ISSN: 1535-864X, URL: ftp://download.intel.com/technology/itj/2002/volume06issue01/art04_fortrancompiler/vol6iss1_art04.pdf			



Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.